Centre for High Performance Computing 2020 National Conference



Contribution ID: 170 Type: Talk

AMD's Heterogeneous Path to Exascale

Tuesday, 1 December 2020 15:45 (30 minutes)

Tomorrow's supercomputers will need to leverage the power of heterogeneous architectures in more graceful ways than what can be done today. Doing so will improve the trajectory of future performance gains.

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Session Classification: HPC

Track Classification: HPC Technology