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Towards Scalable Comparison of Large Scale Graphs

Keywords: Large Scale Graphs, Graph Comparison, Shared Memory Implementation Authors: Krishna Sai Ujwal Kambhumpati(1), Patrick Bell(2)), Michela Taufer(2) , and Sanjukta Bhowmick(1) Affiliations: 1. The University of North Texas 2. The University of Tennessee, Knoxville Corresponding Email Address: krishnasaiujwalkambhumpati@my.unt.edu Comparing the structure of a pair or a group of graphs is a fundamental problem in graph theory, and has manv applications ranging from bioinformatics, cybersecurity and recently in assessing non-determinism in HPC simulations [1,2]. There exist several approximate algorithms and heuristics to compare graphs, based on different metrics of similarity. Among them, one of the popular approaches is to compare motifs (or graphlets) in each graph. Motifs are subgraphs of small size. Since the cost of computing motifs increases exponentially as the size increases. typically the size of the motif is restricted to five vertices. Each motif is associated with a set of unique automorphism orbits, that denote the position of the vertices within the structure of the motif. As presented in [3], each vertex is associated with a graphlet degree distribution vector(GDV). A GDV of a vertex is the number of times that the vertex is associated an automorphism orbit. Vertices with similar GDVs, tend to have the same local structure. Given a pair of graphs, the algorithm aims to align similar vertices from each graph, to evaluate the similarity between them. Existing approaches of GDV computation does not scale to large graphs. First, the larger the graph, the more motifs have to be computed. Second, due to the automorphism of many small motifs, several redundant and overlapping computations occur. We propose a novel scalable algorithm to address these challenges by representing motif computation as tree traversal and leveraging the vertex ids to eliminate redundant computations. In our algorithm, a vertex is designated as a source node. The graph is traversed from this source node to create all possible trees of size five or less, with the constraint that a leaf node in a tree cannot have an id that is lower than the source node. This ensures that trees generated from a source node are unique to that node only. Each tree is a motif and the positions of the vertices in the motif are computed to update their respective GDVs. In the next stage, back edges are added to the trees, with the condition that the back edge connects the vertices with the two lowest ids in the

cycle. Again, this condition ensures that motifs with cycles are formed exactly once, and not repeated.

Our proposed algorithm has several advantages. First, our algorithm eliminates redundant computations by ensuring

each motif is seen exactly once. Second, the computations can be easily parallelized per vertex leading to scalable

executions. Third, our method allows us to prioritize which vertices to process earlier, based on vertex based properties, such as degree distribution. Finally, our method is flexible enough to find motifs in attributed as well as

non-attributed graphs. So far most scalable algorithms are designed either for strictly attributed graphs (GraphPI [4])

or non-attributed graphs (PruneJuice [5]). Our method is a more generalized approach. In this presentation, we will

provide an overview of our algorithm, along with strong scalability results on large graphs for shred memory systems,

and demonstrate how our method can be used to compare across event graphs arising from large HPC simulations.

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